

Fig. 1

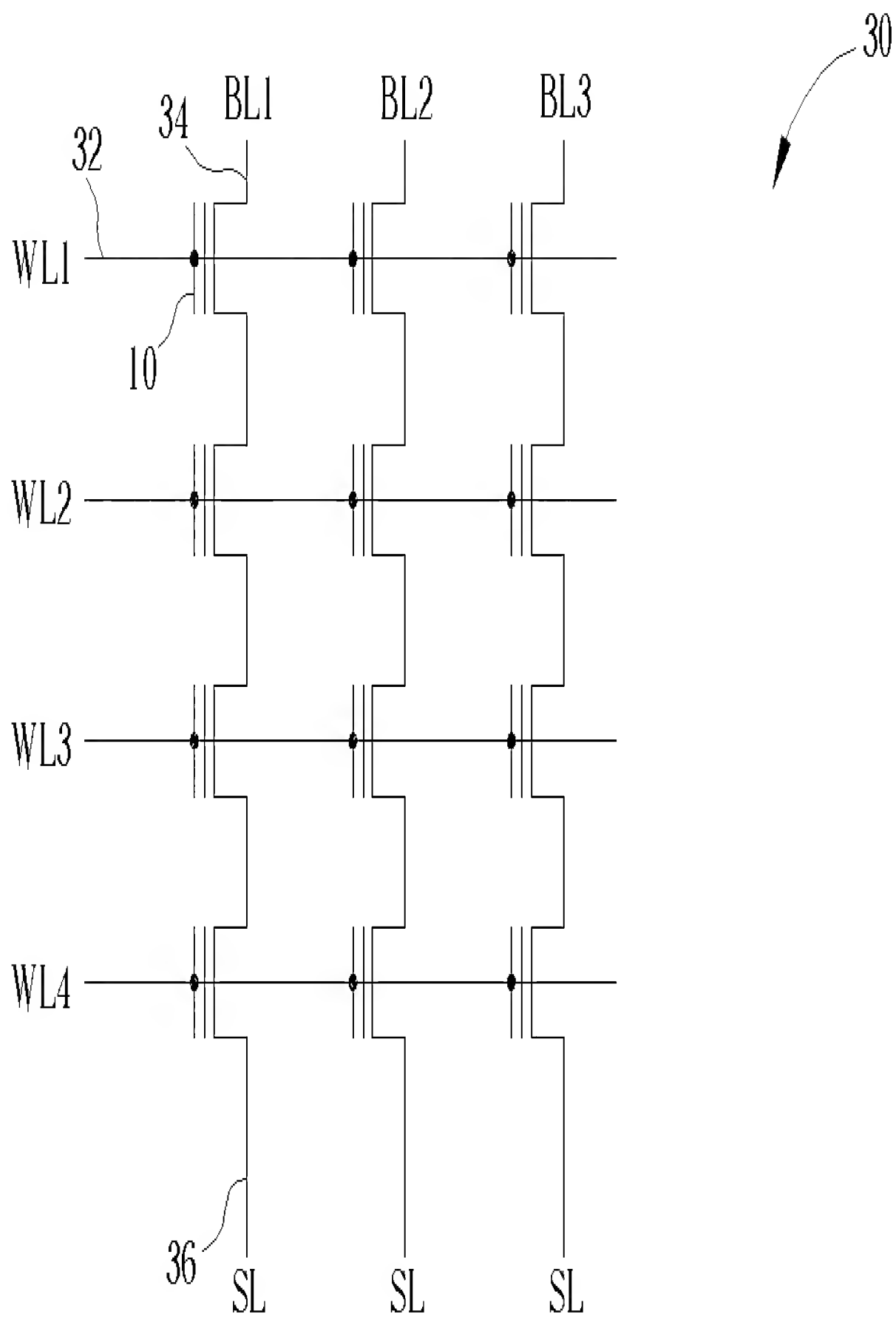


Fig. 2

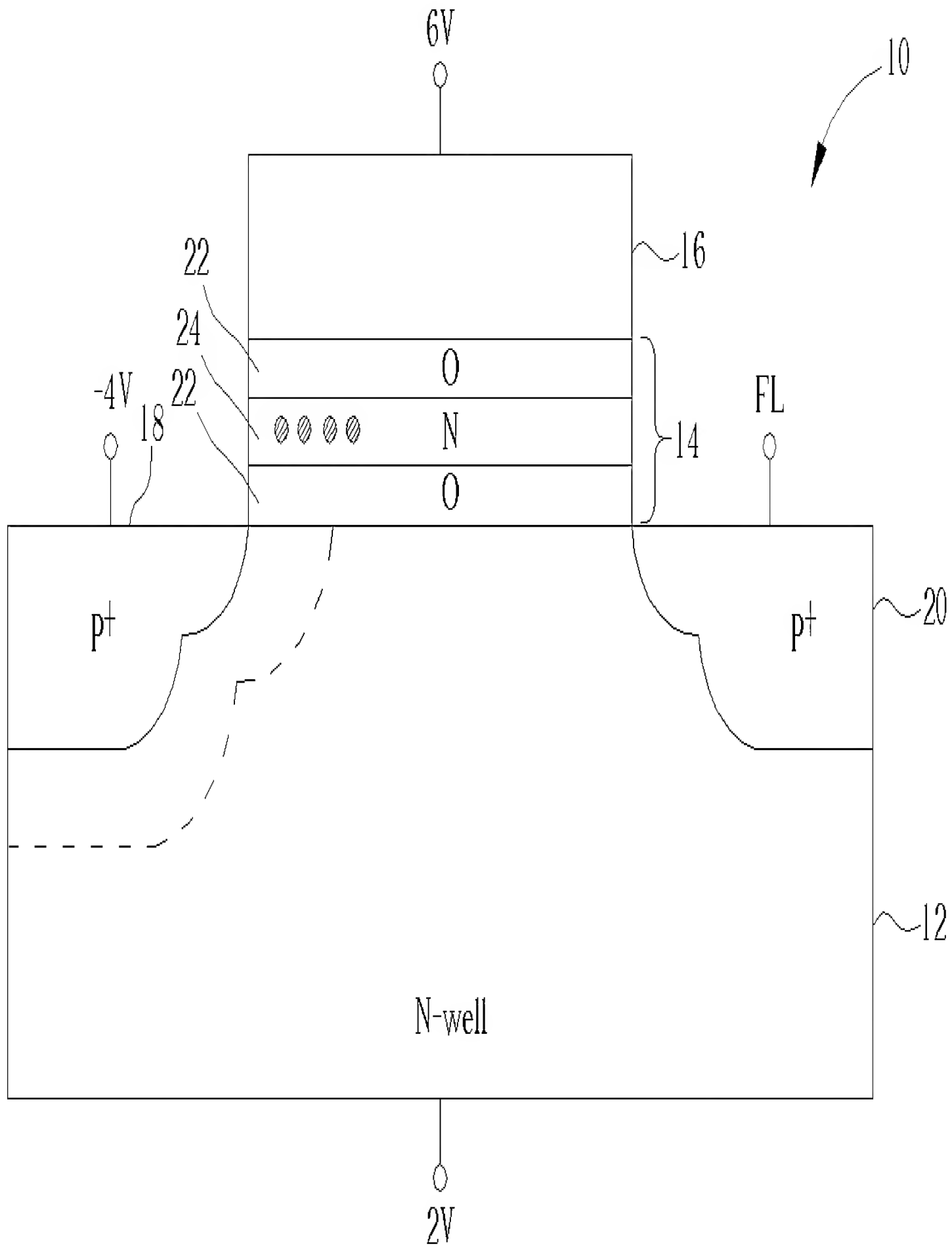


Fig. 3

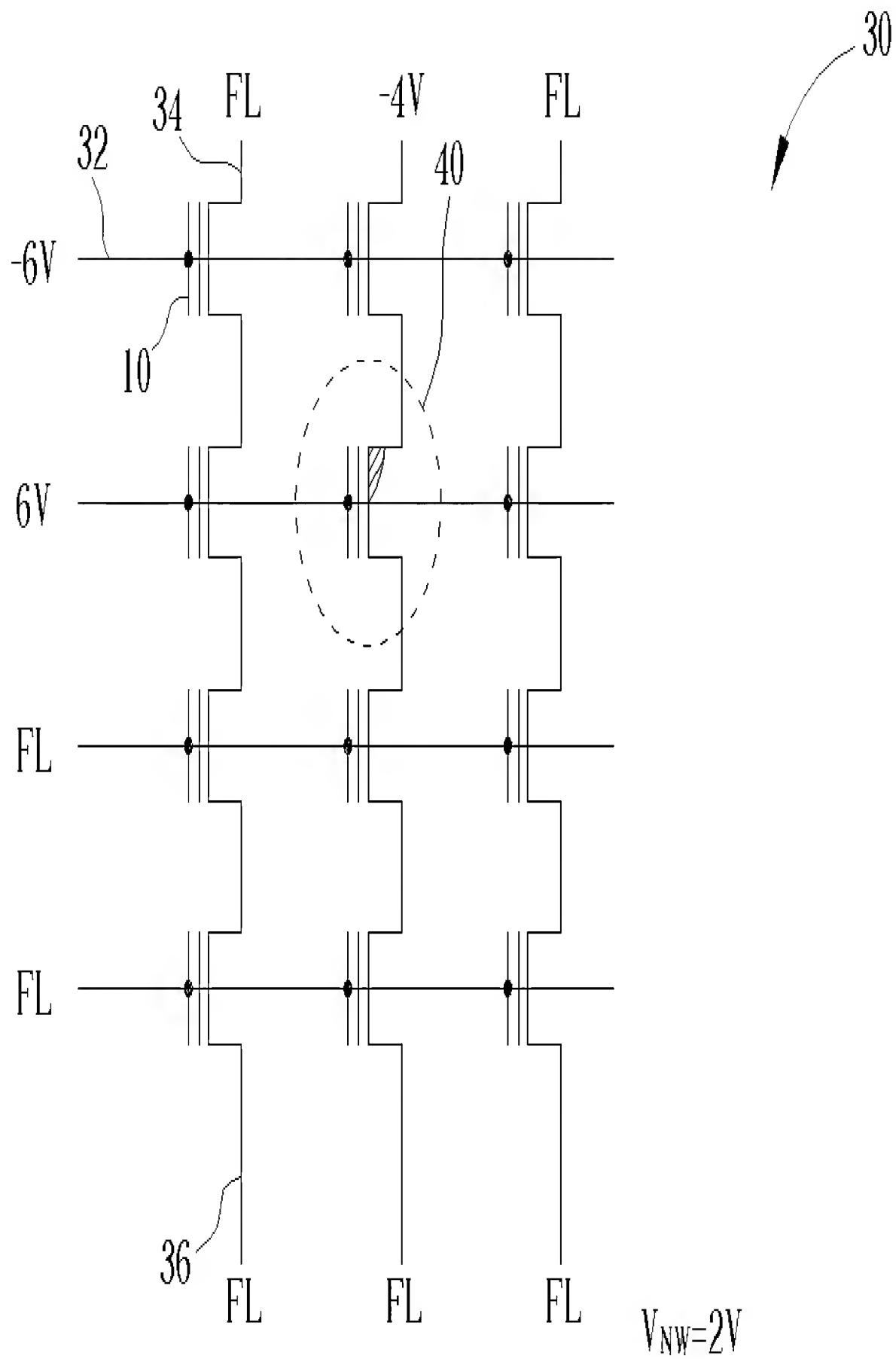


Fig. 4

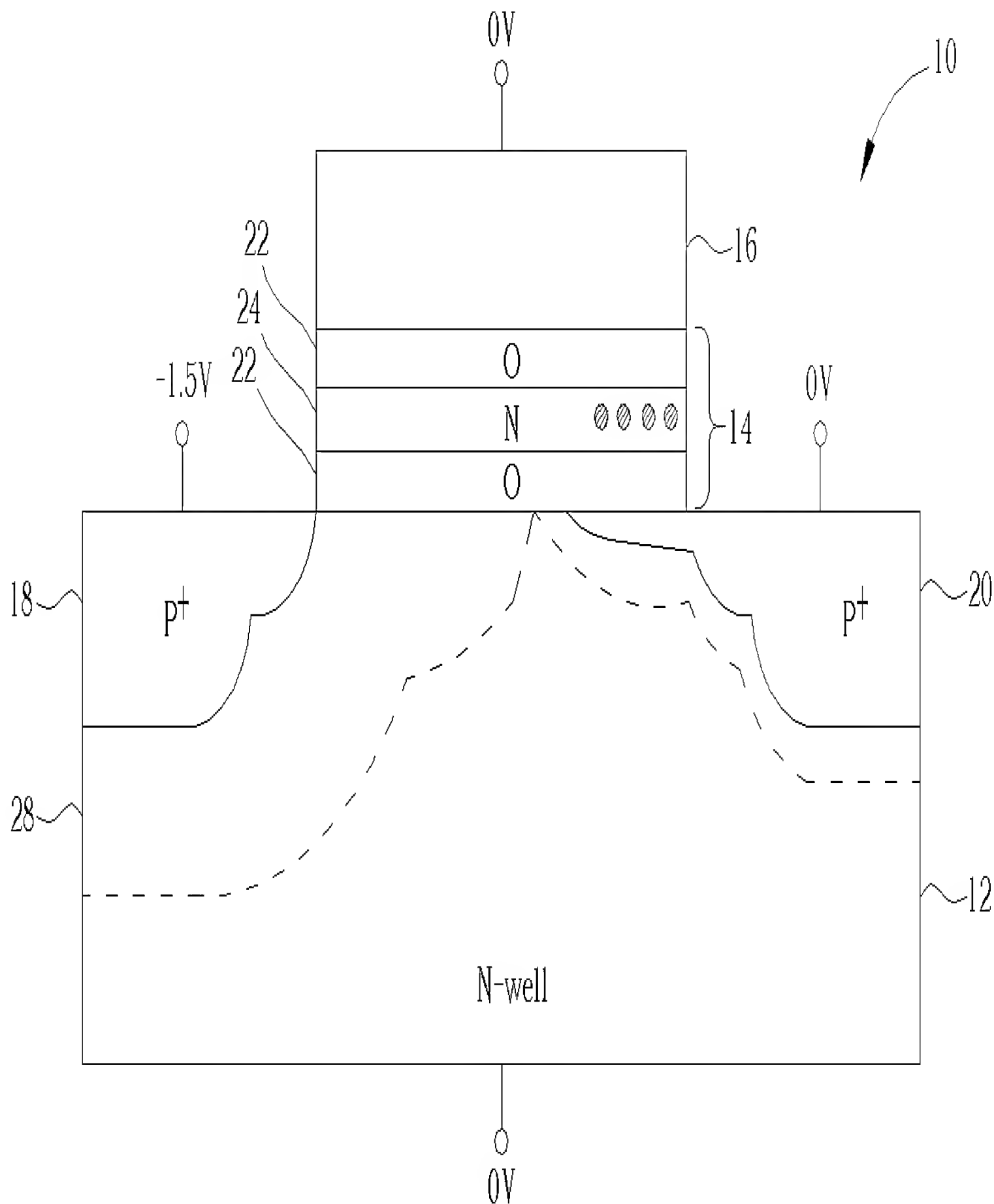


Fig. 5

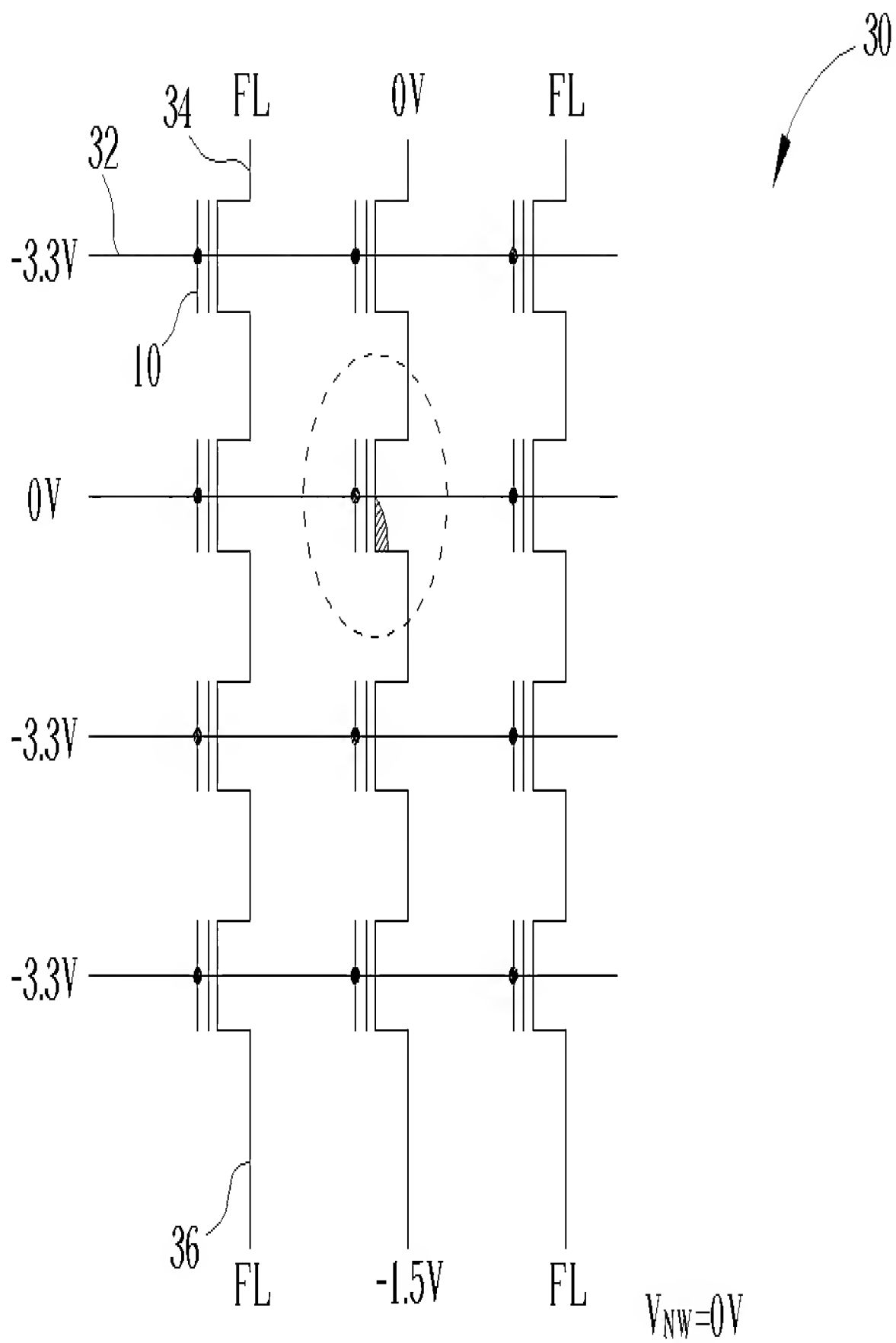


Fig. 6

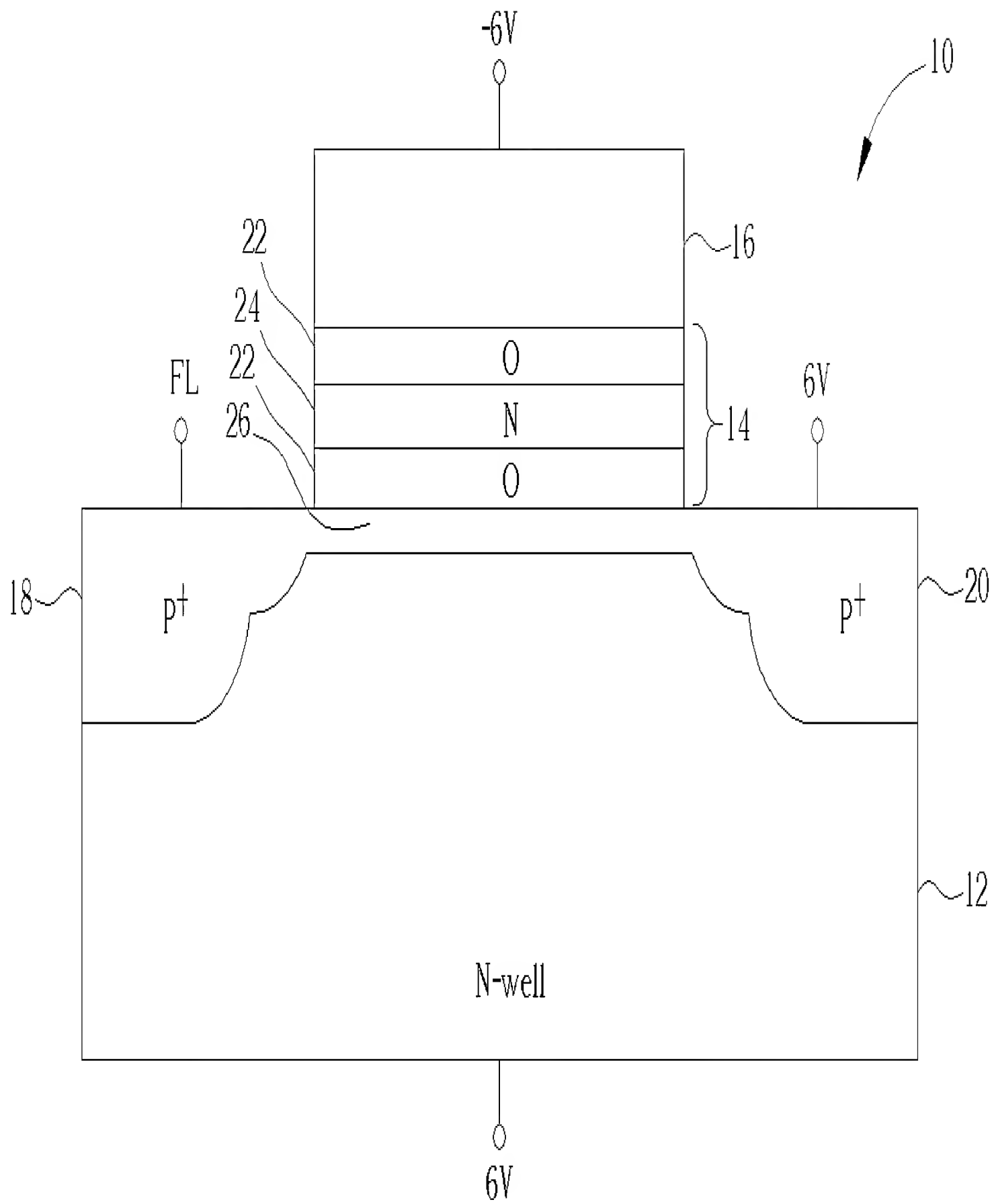


Fig. 7

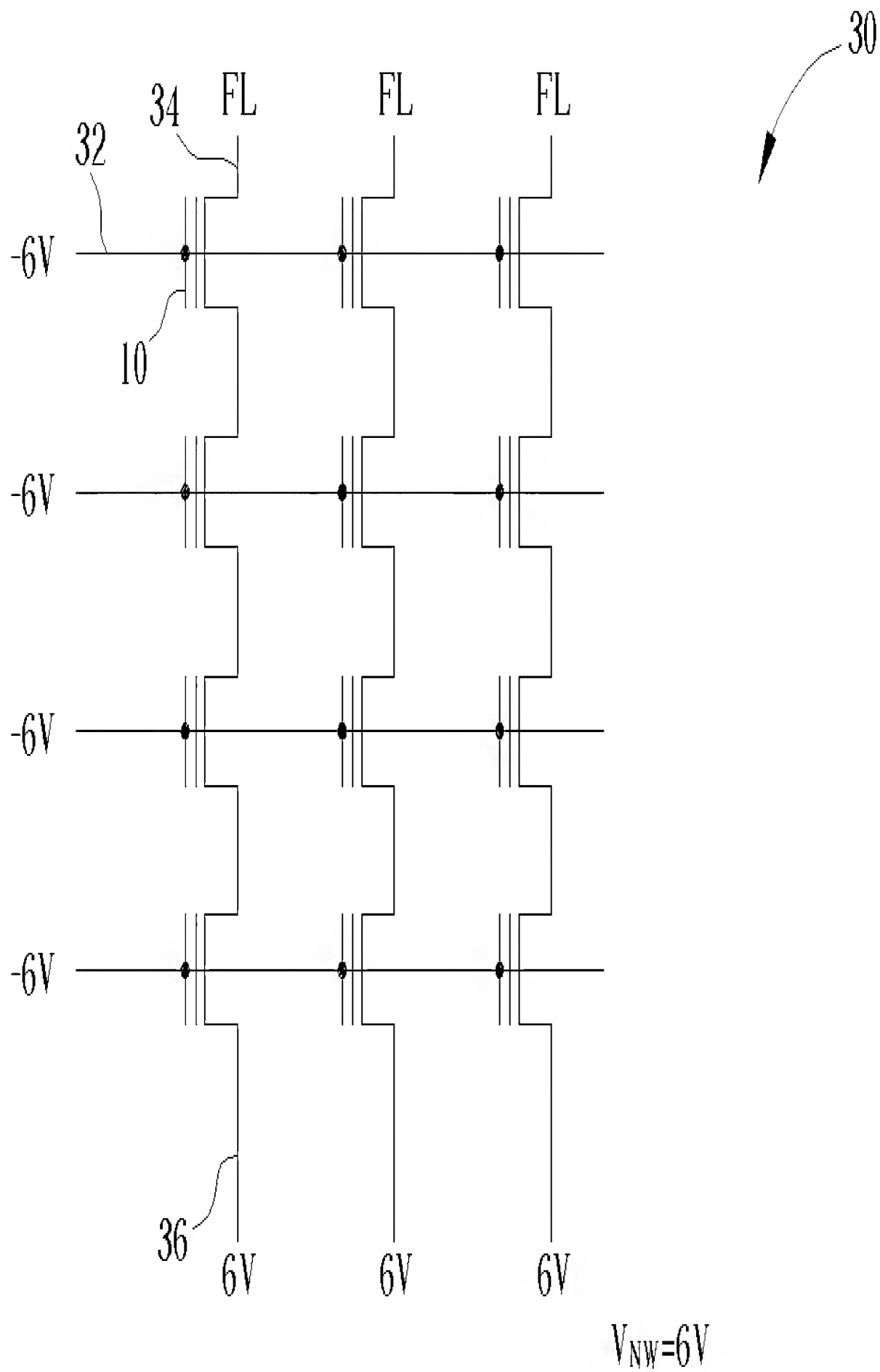


Fig. 8

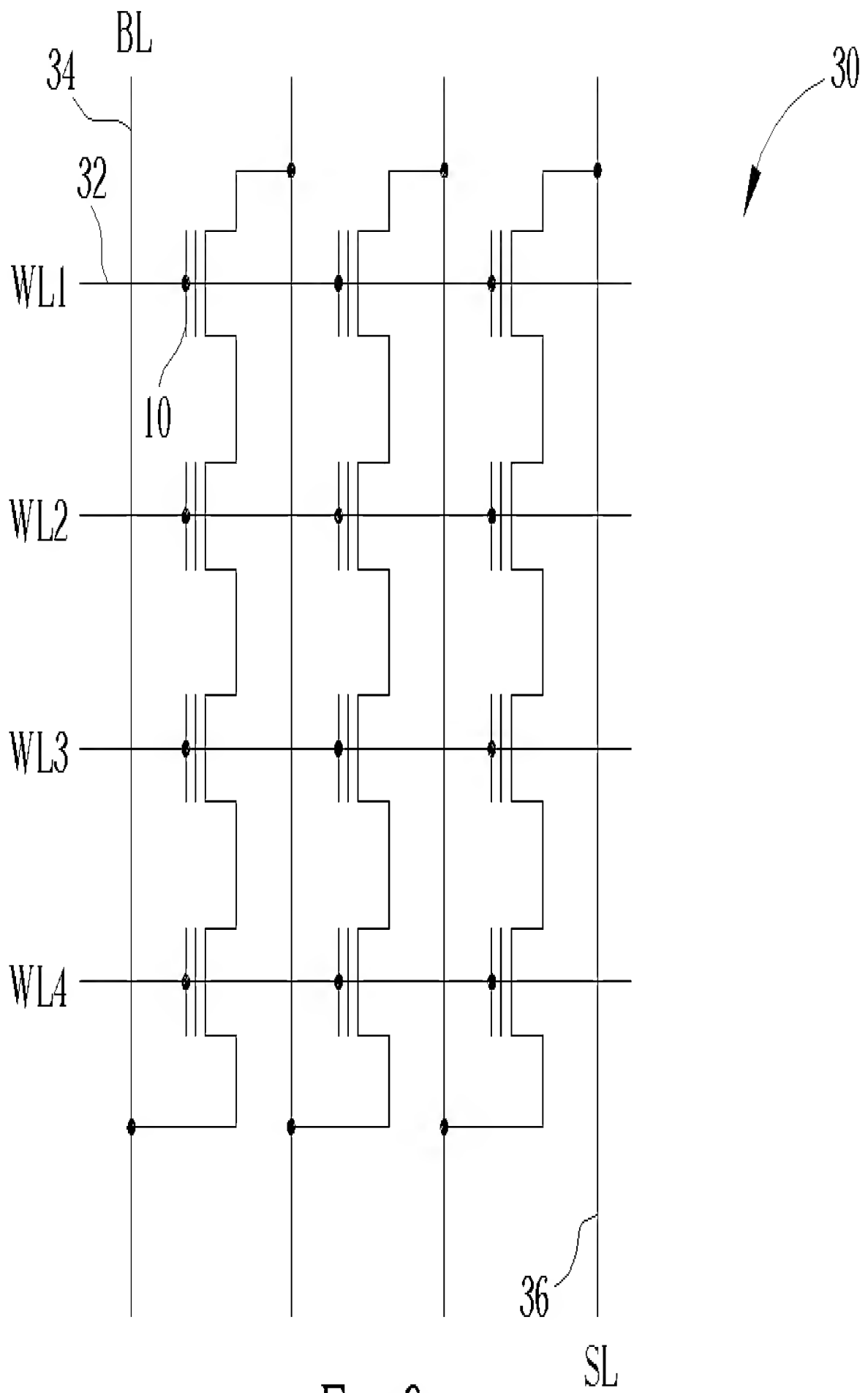


Fig. 9

Voltage	Write	Read	Erase
V_G	6	0	-6
V_1	-4	0	FL
V_2	FL	-1.5	6
V_{NW}	2	0	6
WL1	-6	-3.3	-6
WL2	6	0	-6
WL3	FL	-3.3	-6
WL4	FL	-3.3	-6
BL1	FL	FL	FL
BL2	-4	0	FL
BL3	FL	FL	FL
SL1	FL	FL	6
SL2	FL	-1.5	6
SL3	FL	FL	6

Fig. 10